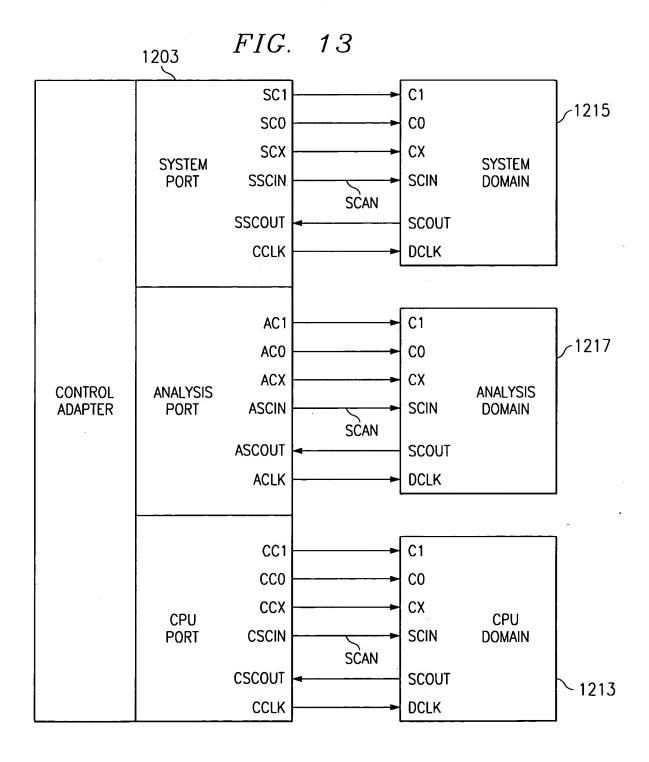


FIG. 10



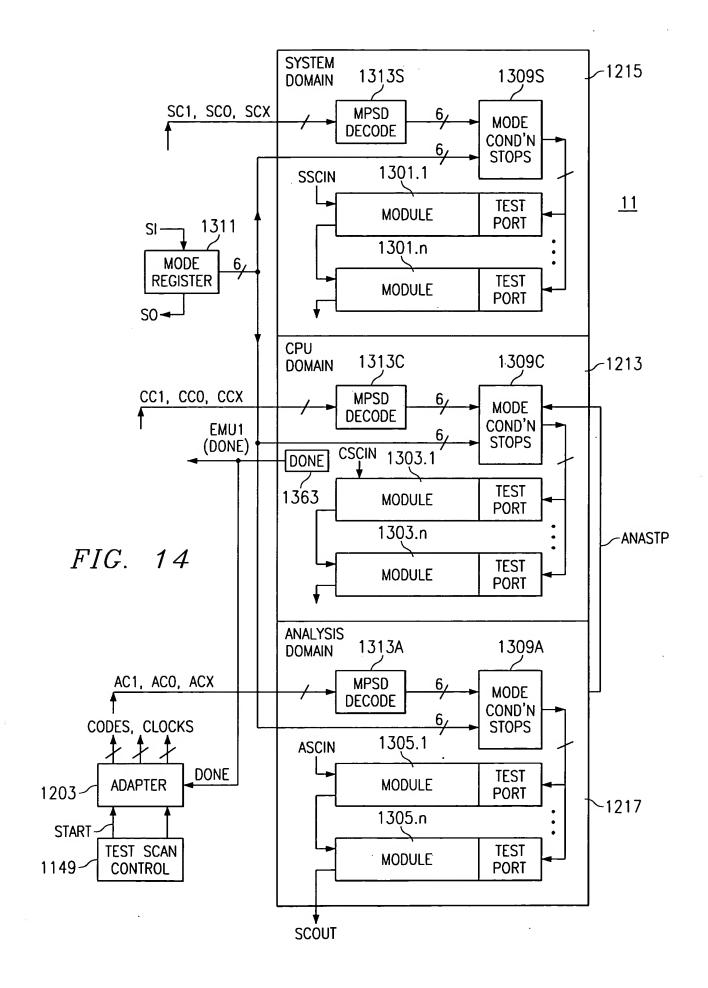
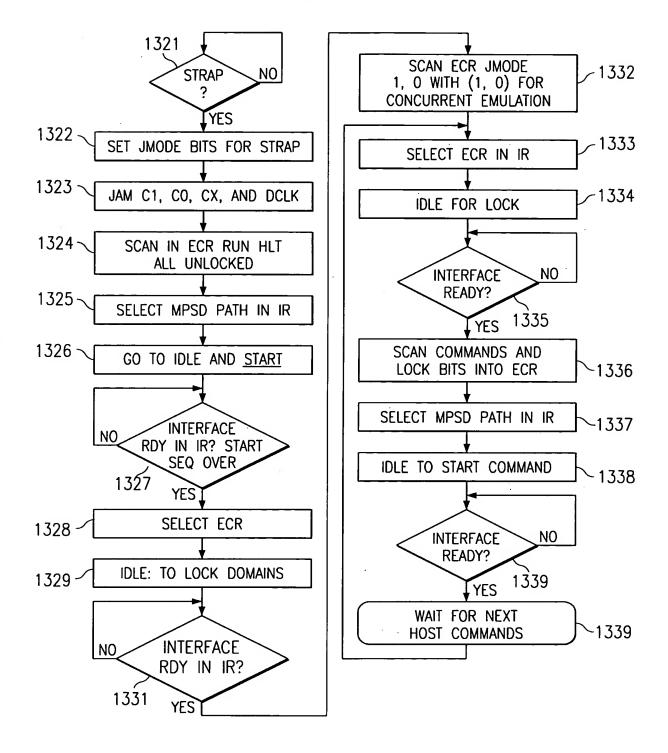
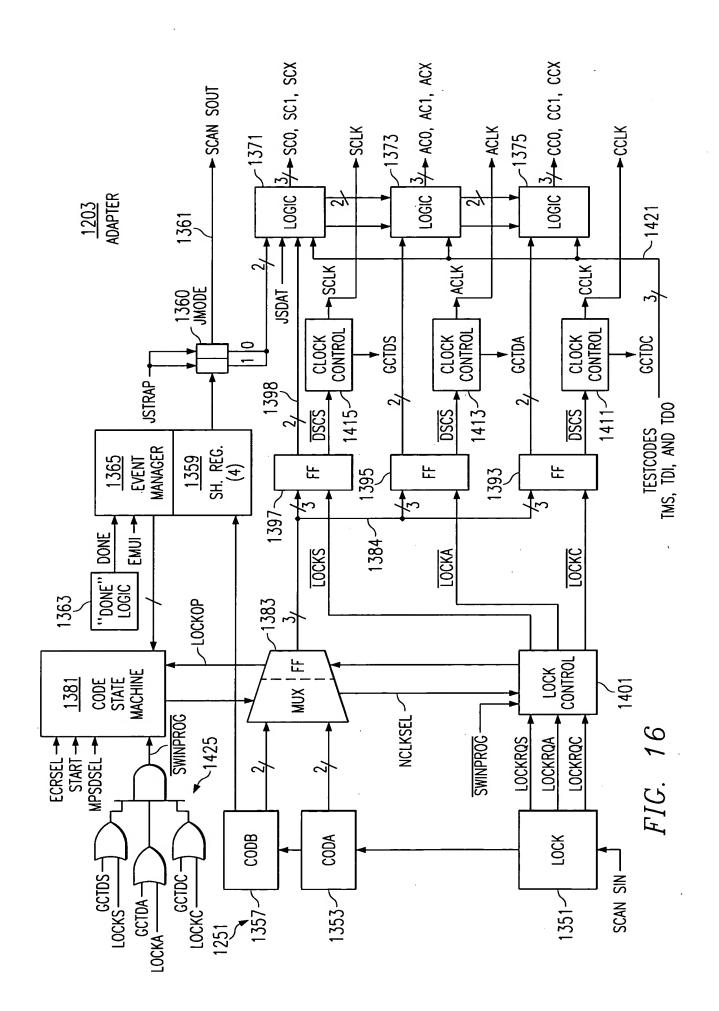
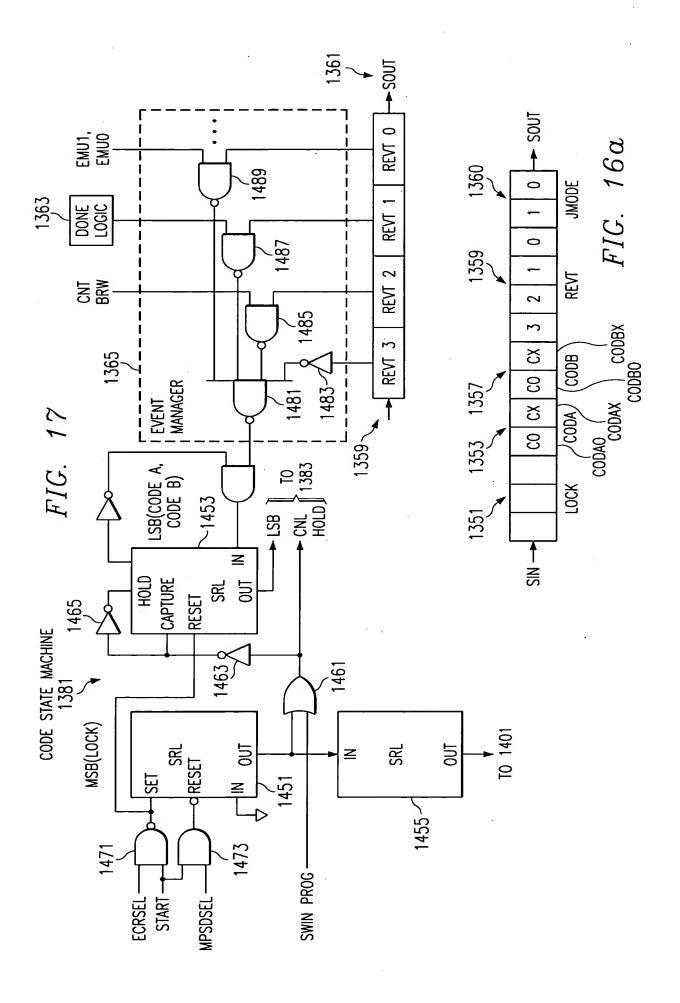
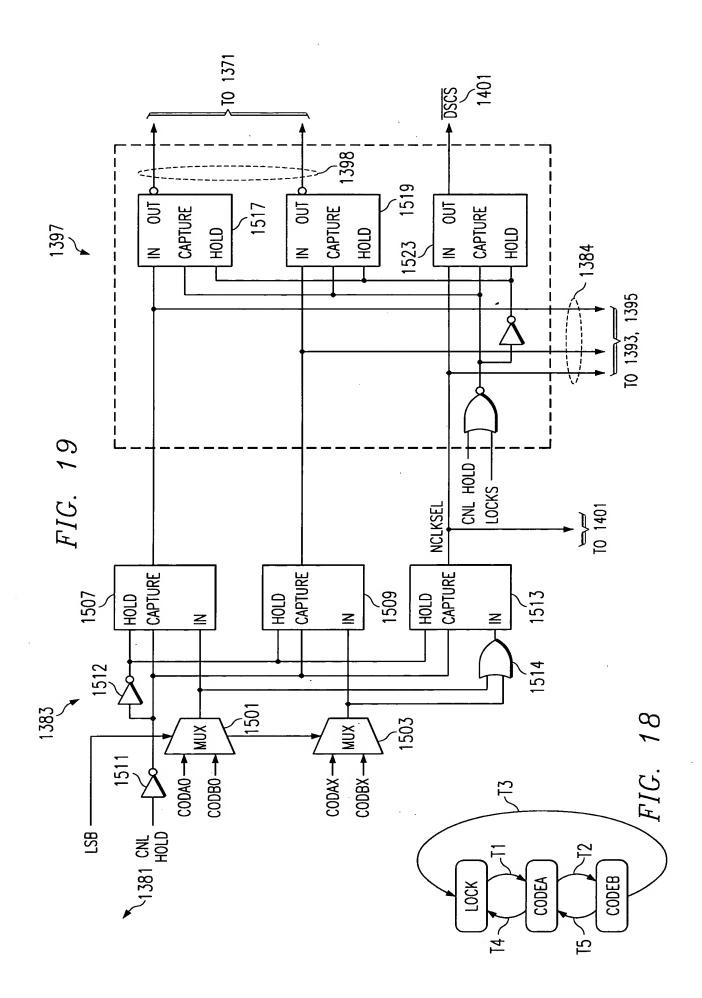


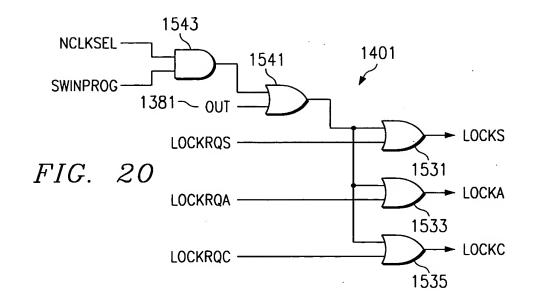
FIG. 15

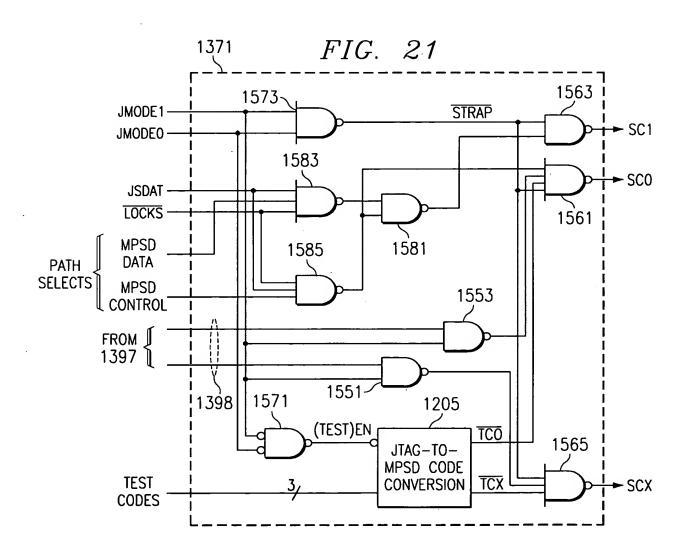


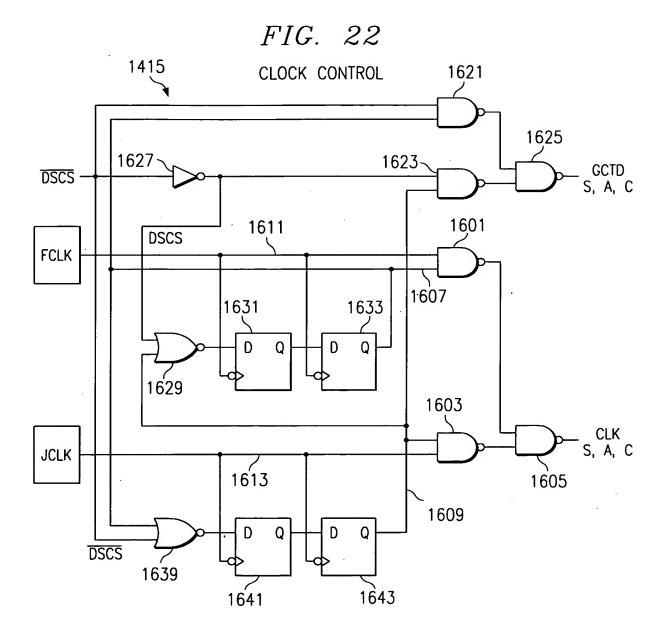












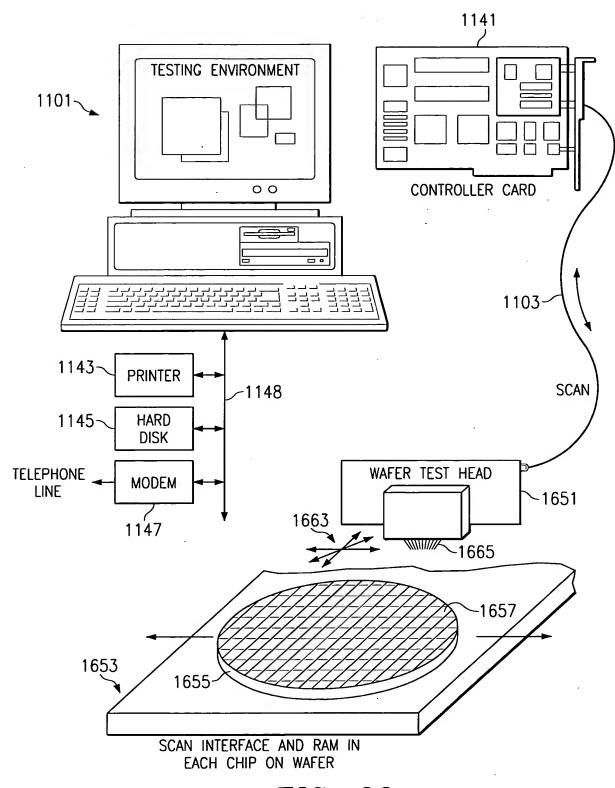
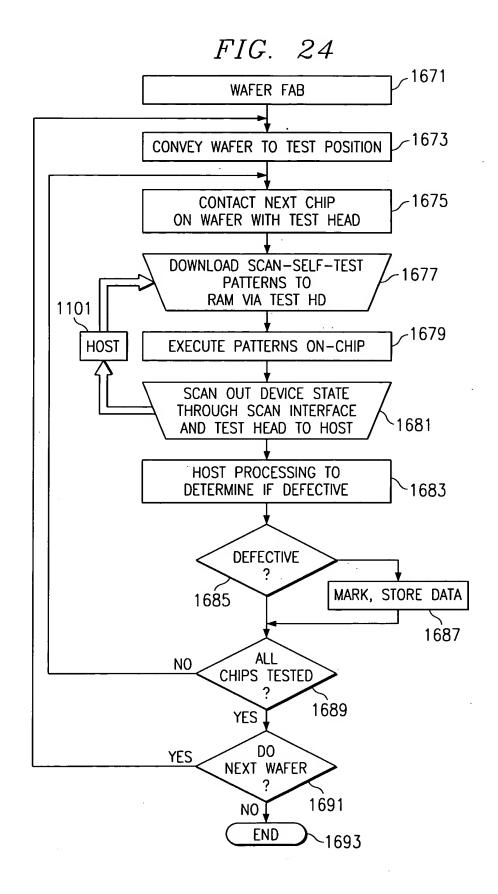
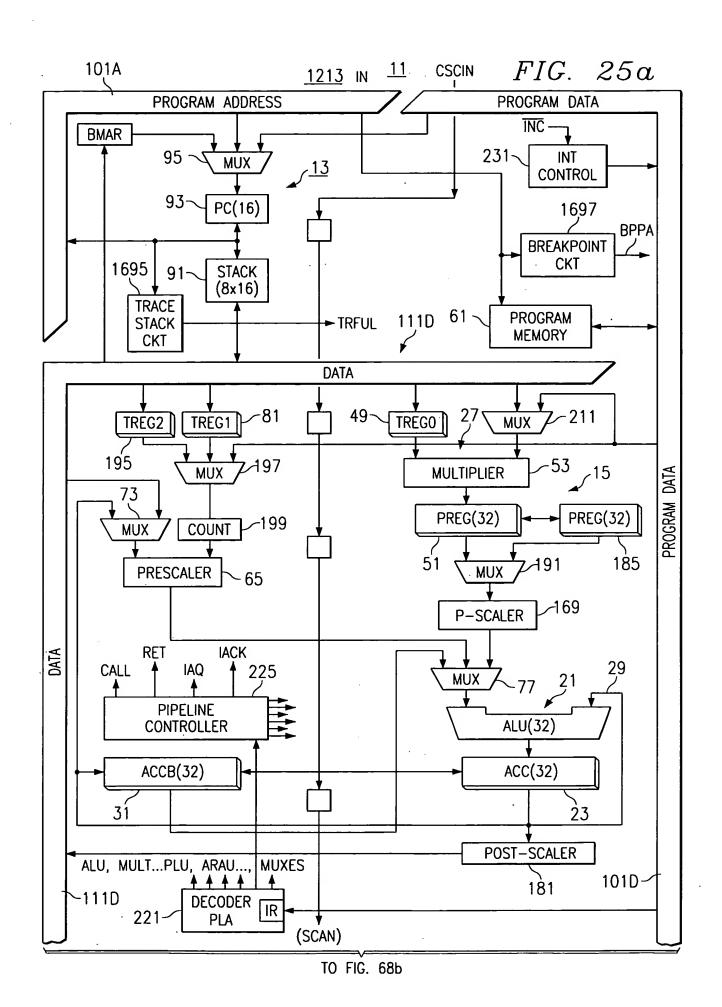
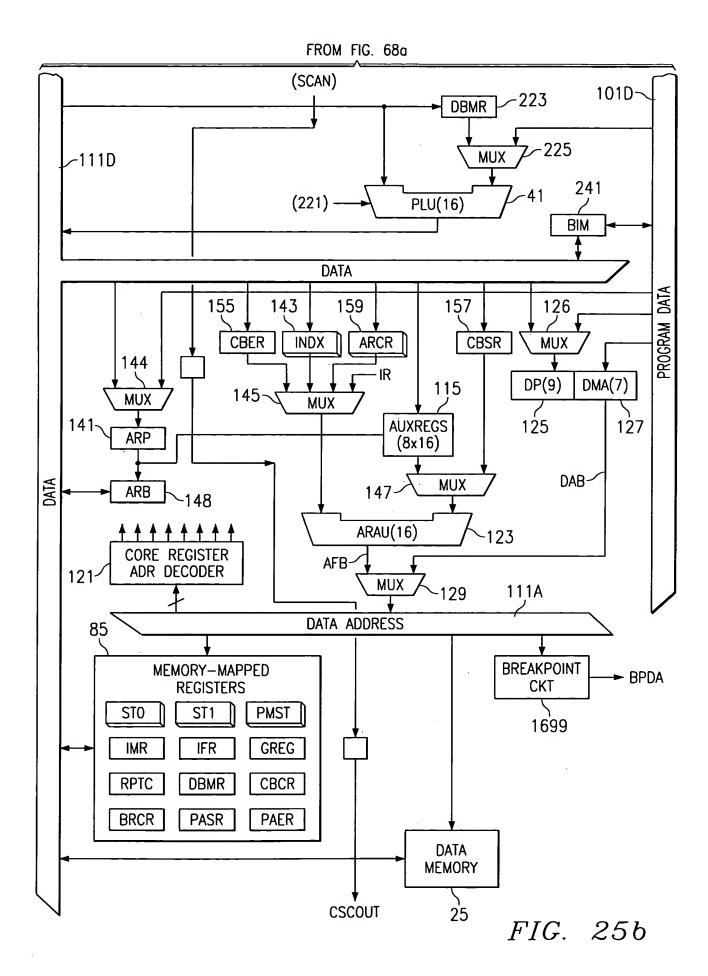


FIG. 23







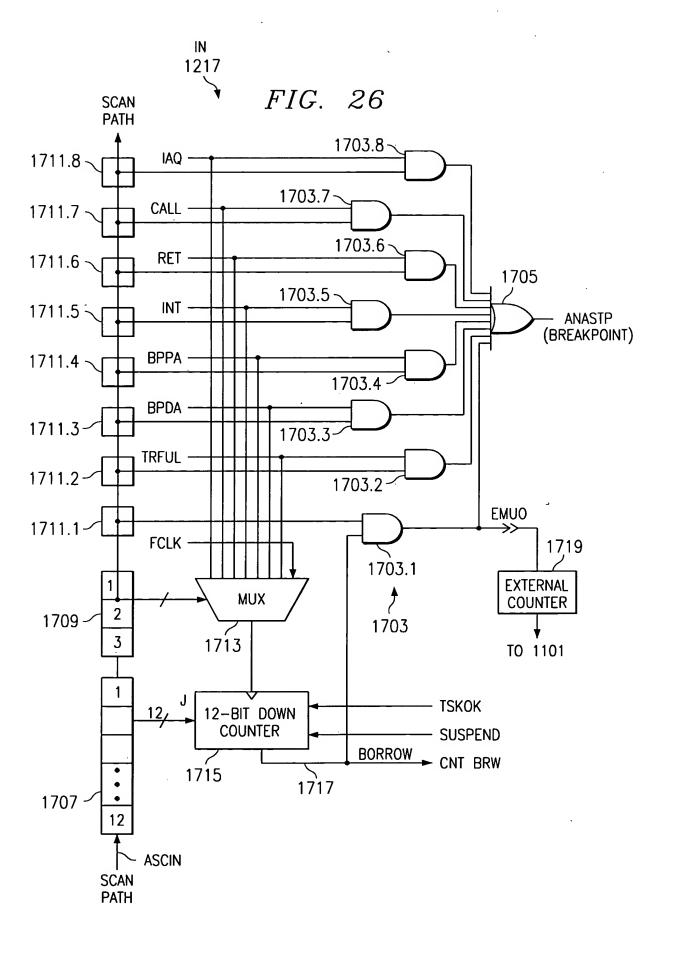
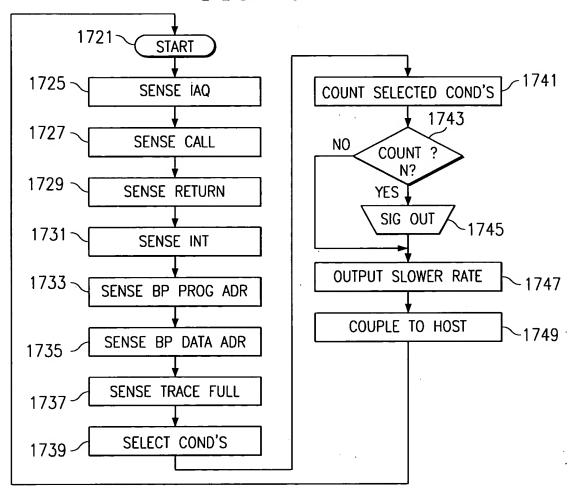
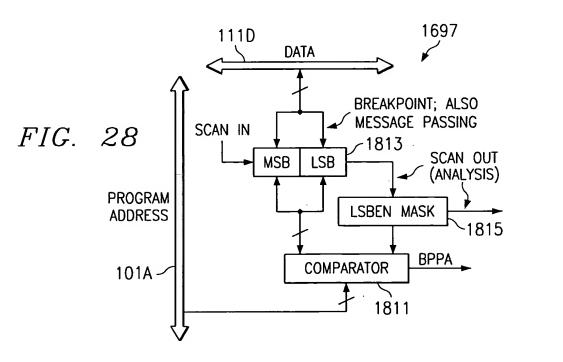
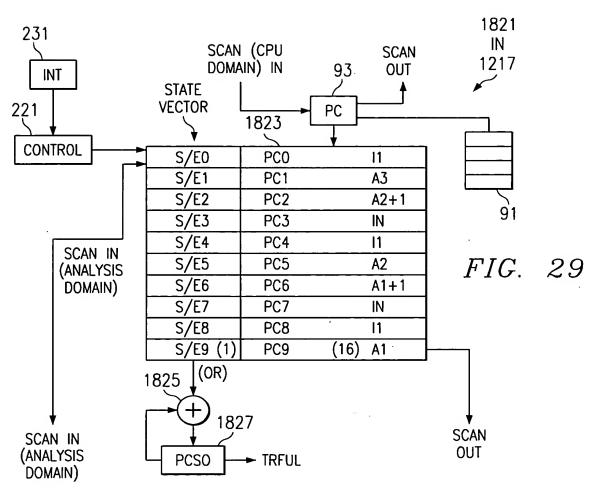
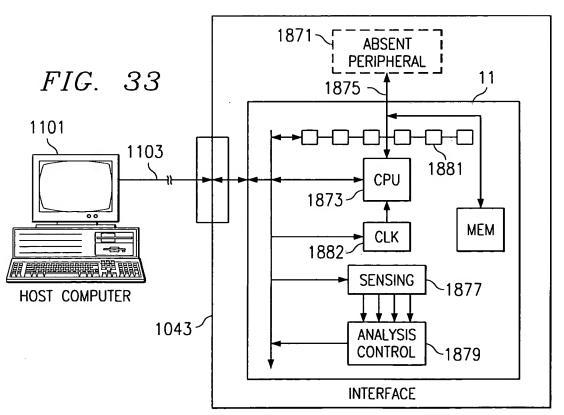


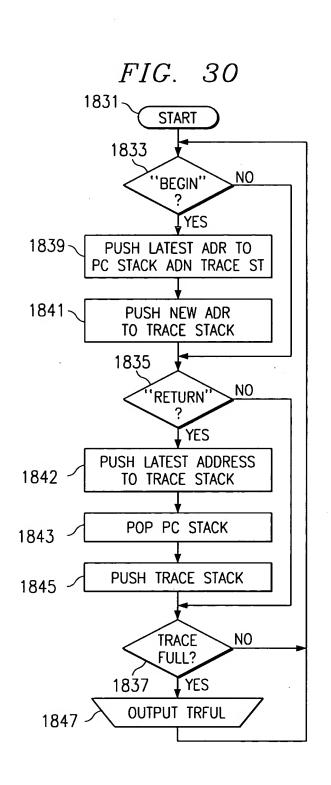
FIG. 27

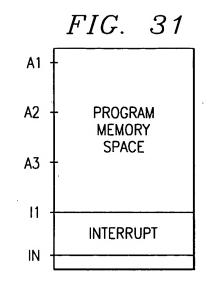


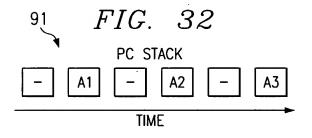












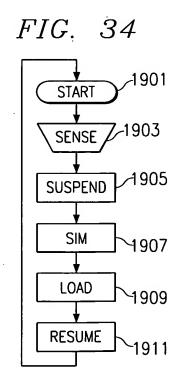


FIG. 35

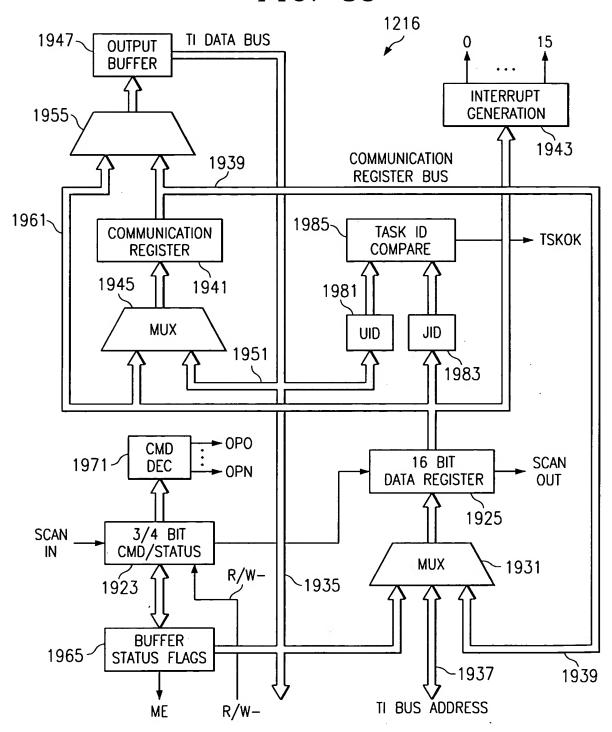
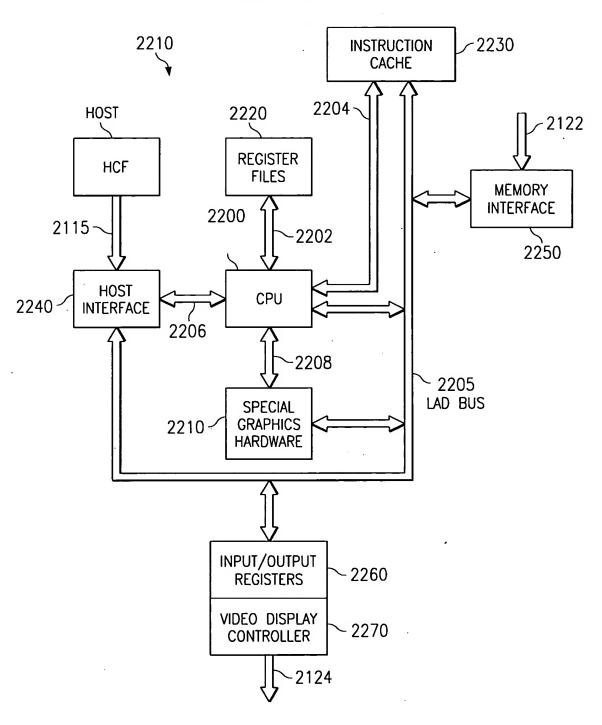
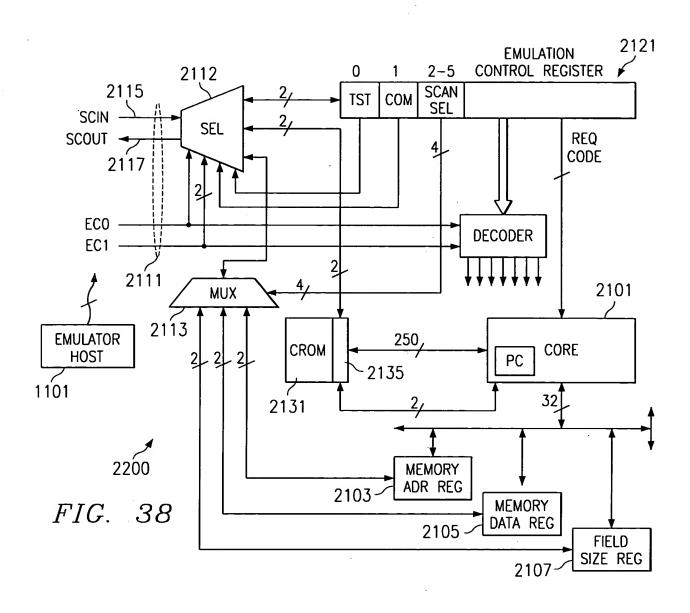
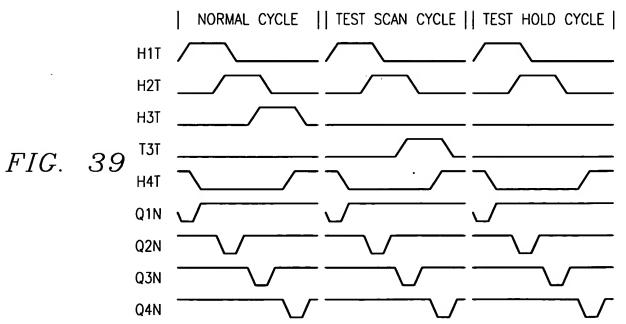


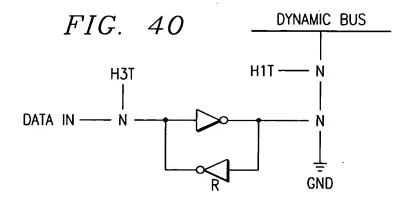
FIG. 36 2001-START 2003 LOAD HOST COMPUTER WITH MULTIPURPOSE SOFTWARE FOR SCAN CONTROL 2005-COUPLE FUNC CIRCUITRY TO SCAN OF HOST 2007 **SELECT PROGRAM** 2021 2025 2017 2013 2009 **TEMPORARY** TEST FAB/ COMM'N CO-SIMULATION **EMULATION** 000 MFR/FIELD 1/0 **PROCESSOR** 2015 2019 2023 \ DATA STRUC **MACHINE MESSAGE SIMULATED TRANSFERS** STATE **MACHINE** PASSING TO PERIPHERAL AND HOST CRT, PRTR, **TRANSFERS STATE PROCESSING ACCESS** RUN, HALT **TRANSFERS MODEM** 2027 2011 DO YES MORE? 2029 NO **END** 2031-

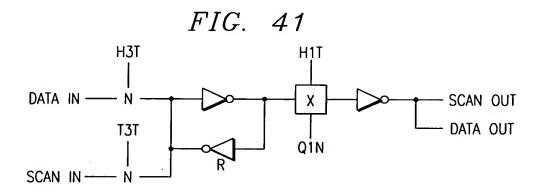
FIG. 37











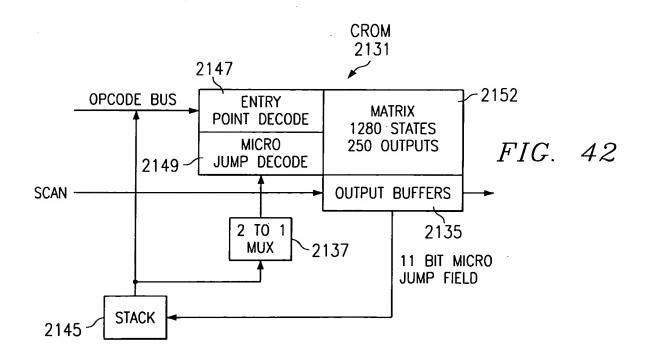


FIG. 43

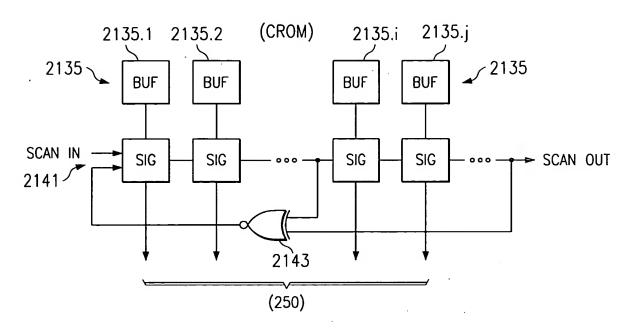


FIG. 44 H3T FROM CROM Ν OUTPUT H1T C3T 2151 2153 SCAN OUT TO NEXT STAGE Χ Ν XOR T3T Q1N SCAN IN FROM PREVIOUS N STAGE